

**S/N Unknown**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Giulio-Giuseppe Marotta et al.	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	303.698US2
Title:	ENHANCED PROTECTION FOR INPUT BUFFERS OF LOW-VOLTAGE FLASH MEMORIES		

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

Pursuant to 37 C.F.R. §1.98(d), copies of the listed documents are not provided as these references were previously cited by or submitted to the U.S. Patent Office in connection with Applicants' prior U.S. application, Serial No. 09/651478, filed on August 30, 2000, which is relied upon for an earlier filing date under 35 U.S.C. §120.

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The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

GIULIO-GIUSEPPE MAROTTA ET AL.

By their Representatives,

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Date 29 September 2003

By

  
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Date of Deposit: September 29, 2003

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

<b>Application Number</b>	Unknown
<b>Filing Date</b>	Even Date Herewith
<b>First Named Inventor</b>	Marotta, Giulio-Giuseppe
<b>Group Art Unit</b>	0
<b>Examiner Name</b>	Unknown

Sheet 1 of 2

Attorney Docket No: 303.698US2

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
	EP-0831439	03/25/1998	Suzuki, T.	G08 B	3/10	
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	WO-00/42615	07/20/2000	Tran, H. V., et al.	G11 C	11/56	

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		"1.8 V 0.15 V (Normal Range), and 1.2 - 1.95 V (Wide Range) Power Supply Voltage and Interface Standard for Non-Terminated Digital Integrated Circuit", <u>EIA/JEDEC STANDARD</u> , EIA/JESD8-7, Electronic Industries Association, Engineering Department, (1997), pp. 1-3	

**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)	Complete if Known	
	Application Number	Unknown
	Filing Date	Even Date Herewith
	First Named Inventor	Marotta, Giulio-Giuseppe
	Group Art Unit	0
	Examiner Name	Unknown
Sheet 2 of 2	Attorney Docket No: 303.698US2	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		"Overvoltage Protection Circuit", <u>IBM Technical Disclosure Bulletin</u> , IBM Corp., New York, US, Vol. 30, No. 12, XP002017155, (05 01 1988),	
		"Test Method A114-A, Electrostatic Discharge (ESD) Sensitivity Testing Human Body Model (HBM)", <u>EIA/JEDEC STANDARD</u> , EIA/JESD22-A114-A, Electronic Industries Association, Engineering Department,(1997),pp. 1-9	
		"Test Method A115-A, Electrostatic Discharge (ESD) Sensitivity Testing Machine Model (MM)", <u>EIA/JEDEC STANDARD</u> , EIA/JESD22-A115-A, Electronics Industries Association, Engineering Department,(1997),pp. 1-9	
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EXAMINER

DATE CONSIDERED

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